Docket No.: 43889-951

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Koji ERIGUCHI, et al

Serial No.: 09/610,6

Filed: July 05, 2000

Group Art Unit: 1763

Examiner: EVERHART, Caridad

For:

APPARATUS AND METHOD FOR OPTICAL EVALUATION, APPARATUS AND METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE, METHOD OF

CONTROLLING APPARATUS FOR MANUFACTURING SEMICONDUCTOR

DEVICE, AND SEMICONDUCTOR DEVICE

INFORMATION DISCLOSURE STATEMENT

OCT 1 7 2002

Commissioner for Patents Washington, DC 20231

TC 1700

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection or Notice of Allowance.

In accordance with 37 CFR 1.17(p), please charge the fee of \$180.00 to Deposit Account RECEIVED AND ROOM No. 500417.

WDC99 666690-1.043889.0951

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

Michael E. Fogarty

Registration No. 36,139

600 13th Street, N.W. Washington, DC 20005-3096 (202) 756-8000 MEF:men

Facsimile: (202) 756-8087 **Date: October 15, 2002**

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Docket No.: 43889-951

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Koji ERIGUCHI, et al.

Serial No.: 09/610,640

Filed: July 05, 2000



Group Art Unit: 1763

Examiner: EVERHART, Caridad

For:

APPARATUS AND METHOD FOR OPTICAL EVALUATION, APPARATUS AND METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE, METHOD OF CONTROLLING APPARATUS FOR MANUFACTURING SEMICONDUCTOR DEVICE, AND SEMICONDUCTOR DEVICE

THE COMMISSIONER FOR PATENTS AND TRADEMARKS

Washington, DC 20231

RECEIVED

Dear Sir:

OCT 1 7 2002

TC 1700

Transmitted herewith is an Information Disclosure Statement in the above identified application.

No additional fee is required.

Applicant is entitled to small entity status under 37 CFR 1.27 Also attached: Form PTO-1449 and twelve cited references

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	13	20	0	\$18.00 =	\$0.00
Independent Claims	2	3	0	\$84.00 =	\$0.00
		Multiple claims newly presented			\$0.00
		Fee for extension of time			\$0.00
		Information Disclosure Statement			\$180.00
		Total of Above Calculations			\$180.00

Please charge my Deposit Account No. 500417 in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.

The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

Michael E. Fogarty

Registration No. 36,139

600 13th Street, N.W. Washington, DC 20005-3096 (202) 756-8000 MEF:men Facsimile: (202) 756-8087 **Date: October 15, 2002** RECEIVED ROOM ROOM